



Datasheet
SFP+-10G-LR-PO

01

Product features

- 10Gb/s serial optical interface compliant to 802.3ae 10GBASE-LR
- 1310nm DFB transmitter, PIN photo-detector
- Duplex LC connector
- Metal enclosure, for lower EMI
- Electrical interface compliant to SFF-8431 specifications for 10 Gigabit SFP+
- 2-wire interface for management specifications compliant with SFF 8472
- Single 3.3V power supply
- Operating temperature range: -40°C to 85°C

02

Applications

- 8G Fibre Channel
- 10.325Gb/s Gigabit Ethernet

Absolute Maximum Ratings

The operation in excess of any absolute maximum ratings might cause permanent damage to this module.

Parameter	Symbol	Min	Max	Units	Notes
Storage Temperature	TS	-40	85	degC	
Operating Case Temperature	TOP	0	70	degC	
Power Supply Voltage	VCC	0.5	4.7	V	
Relative Humidity (non-condensation)	RH	0	85	%	

Electrical Characteristics

Parameter	Symbol	Min	Typical	Max	Units	Notes
Power Supply Voltage	VCC	3.15	3.3	3.6	V	
Supply Current	ICC		185	250	mA	
Transmitter						
Input differential impedance	Rin		100		Ω	
Single ended data input swing	Vin,pp	250		1200	mV	
Transmit Disable Voltage	VD	Vcc-1.3		Vcc	V	
Transmit Enable Voltage	VEN	Vee		Vee+ 0.8	V	
Transmit Disable Assert Time				10	us	

Parameter	Symbol	Min	Typical	Max	Units	Notes
Receiver						
Single ended data output swing	Vout,pp	250		800	mV	3
Data output rise time	tr		100	175	ps	4
Data output fall time	tf		100	175	ps	4
LOS Fault	VLOS fault	Vcc-0.5		VccHOST	V	5
LOS Normal	VLOS norm	Vee		Vee+0.5	V	5
Power Supply Rejection	PSR	100			mVpp	6

NOTES

1. Connected directly to TX data input pins. AC coupled thereafter.
2. Or open circuit.
3. Into 100 ohms differential termination.
4. 20 – 80 %
5. Loss Of Signal is LVTTTL. Logic 0 indicates normal operation; logic 1 indicates no signal detected.
6. Receiver sensitivity is compliant with power supply sinusoidal modulation of 20 Hz to 1.5 MHz up to specified value applied through the recommended power supply filtering network.

Optical Characteristics

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Transmitter						
Output Opt. Pwr (End of Life)	POUT	-5.0		0	dBm	1
Optical Wavelength	λ	1270	1310	1360	nm	
Wavelength Temperature Dependence			0.08	0.125	nm/° C	
Spectral Width (-20dB)	σ			3.0	nm	
Optical Extinction Ratio	ER	3.5			dB	
Sidemode Supression ratio	SSRmin	30			dB	
Optical Rise/Fall Time	tr/ tf		100	160	ps	
RIN	RIN			-120	dB/Hz	
Transmitter Jitter (peak to peak)				100	ps	

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Receiver						
Average Rx Sensitivity @ 10.325G	RSENS3			-14.4	dBm	2
Maximum Input Power	PMAX	0.5			dBm	
Optical Center Wavelength	λ_C	1265	1310	1620	nm	
LOS De -Assert	LOSD			-12	dBm	
LOS Assert	LOSA	-30			dBm	
LOS Hysteresis			1.0		dB	

NOTES

1. Class 1 Laser Safety per FDA/CDRH and IEC-825-1 regulations.
2. With worst-case extinction ratio. Measured with a PRBS 27-1 test pattern, @10.325Gb/s, BER<10⁻¹².

Digital Diagnostic Functions

SFP+-10G-LR-PO transceivers support the 2-wire serial communication protocol as defined in the SFP MSA1.

The standard SFP serial ID provides access to identification information that describes the transceiver's capabilities, standard interfaces, manufacturer, and other information.

Additionally, Pureoptics SFP+ transceivers provide a unique enhanced digital diagnostic monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage. It also defines a sophisticated system of alarm and warning flags, which alerts end-users when particular operating parameters are outside of a factory set normal range.

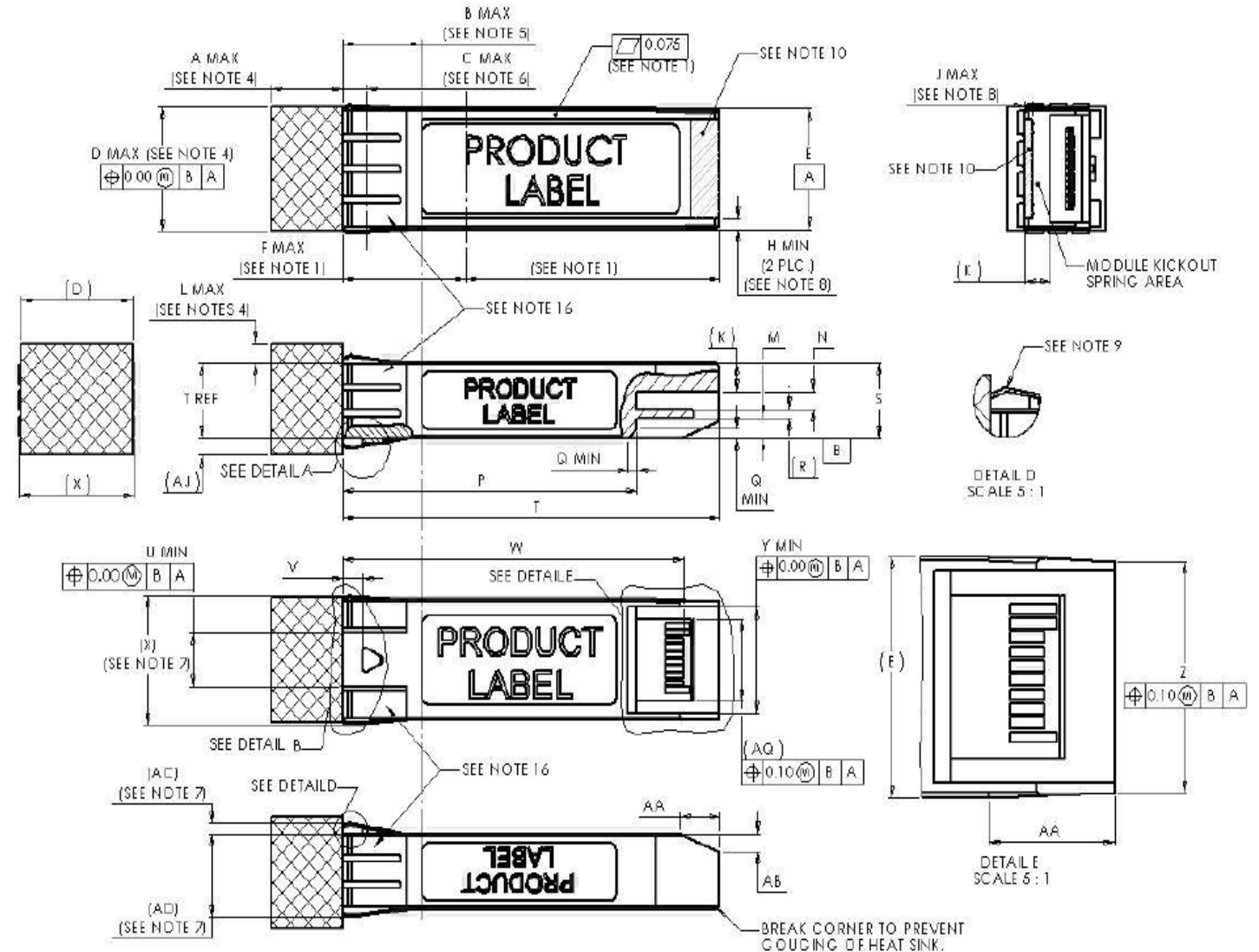
The SFP MSA defines a 256-byte memory map in EEPROM that is accessible over a 2-wire serial interface at the 8 bit address 1010000X (A0h). The digital diagnostic monitoring interface makes use of the 8 bit address 1010001X (A2h), so the originally defined serial ID memory map remains unchanged.

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller (DDTC) inside the transceiver, which is accessed through a 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL, Mod Def 1) is generated by the host. The positive edge clocks data into the SFP transceiver into those segments of the E2PROM that are not write-protected. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

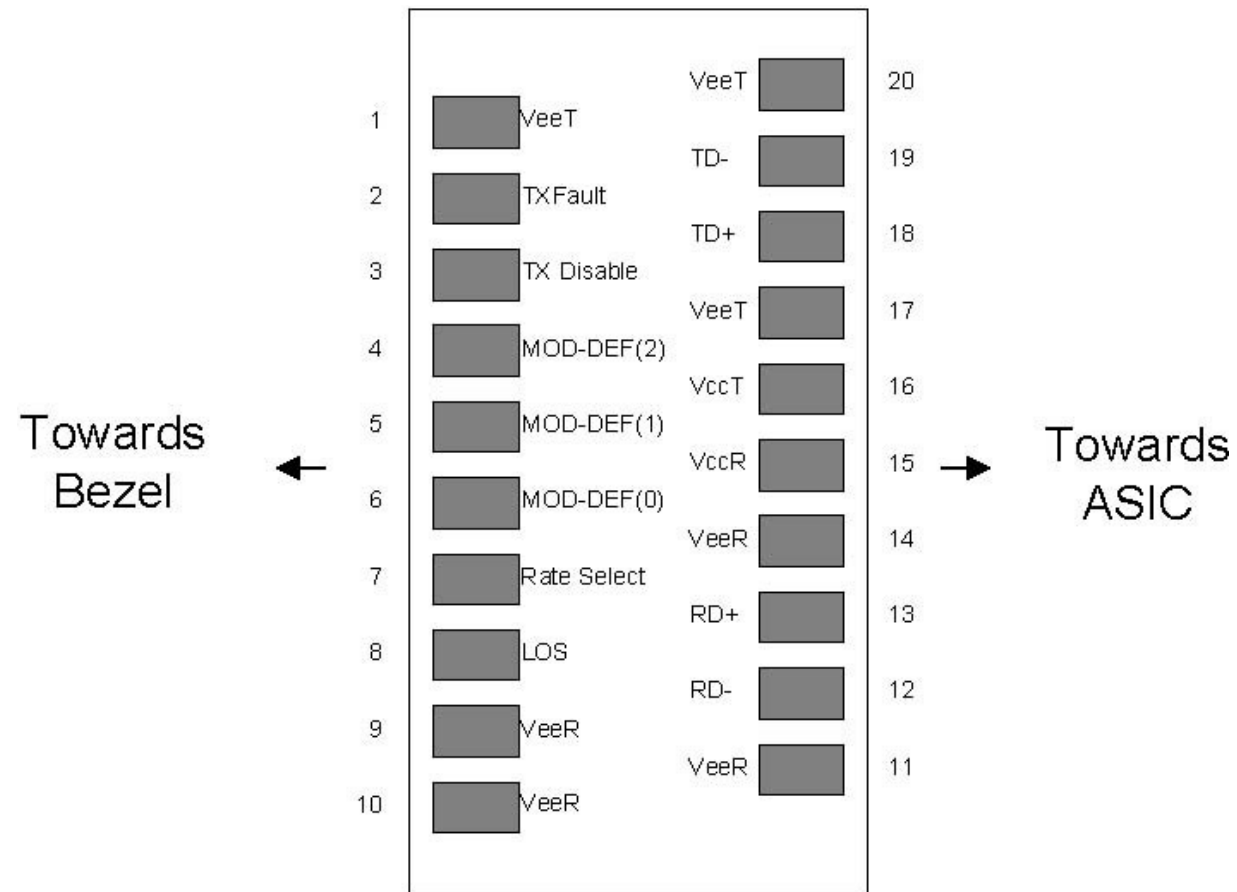
Digital diagnostics for the SFP+-10G-LR-PO are internally calibrated by default.

Mechanical Dimension

Comply to SFF-8432 rev.1.2, the improved Pluggable form factor specification



Pin Assignment and Description



PIN Assignment

PIN #	Symbol	Description	Notes
1	VEET	Transmitter Ground (Common with Receiver Ground)	1
2	TFAULT	Transmitter Fault.	2
3	TDIS	Transmitter Disable. Laser output disabled on high or open.	3
4	MOD_DEF(2)	Module Definition 2. Data line for Serial ID.	4
5	MOD_DEF(1)	Module Definition 1. Clock line for Serial ID.	4
6	MOD_DEF(0)	Module Definition 0. Grounded within the module.	4
7	Rate Select	No connection required	
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.	5
9	VEER	Receiver Ground (Common with Transmitter Ground)	1
10	VEER	Receiver Ground (Common with Transmitter Ground)	1
11	VEER	Receiver Ground (Common with Transmitter Ground)	1
12	RD-	Receiver Inverted DATA out. AC Coupled	

PIN #	Symbol	Description	Notes
13	RD+	Receiver Non-inverted DATA out. AC Coupled	
14	VEER	Receiver Ground (Common with Transmitter Ground)	1
15	VCCR	Receiver Power Supply	
16	VCCT	Transmitter Power Supply	
17	VEET	Transmitter Ground (Common with Receiver Ground)	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	VEET	Transmitter Ground (Common with Receiver Ground)	1

NOTES

1. Circuit ground is internally isolated from chassis ground.
2. TFAULT is an open collector/drain output, which should be pulled up with a 4.7k – 10k Ohms resistor on the host board if intended for use. Pull up voltage should be between 2.0V to Vcc + 0.3V. A high output indicates a transmitter fault caused by either the TX bias current or the TX output power exceeding the preset alarm thresholds. A low output indicates normal operation. In the low state, the output is pulled to <0.8V.
3. Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
4. Should be pulled up with 4.7k - 10 kohms on host board to a voltage between 2.0V and 3.6V. MOD_DEF(0) pulls line low to indicate module is plugged in.
5. LOS is open collector output. Should be pulled up with 4.7k – 10 kohms on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.



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